

REMARKS

First Inventor's Name

Applicants note that the Final Rejection misspells the first named inventor's first name. The correct name is Shunpei Yamazaki, as reflected in the declaration filed in this application. Correction is respectfully requested.

Entry of Amendment And IDS

Since Applicants are filing a RCE herewith, this amendment and accompanying IDS should be entered and considered by the Examiner at this time.

Claim Rejections - 35 USC §112

In the Final Rejection, the Examiner continues to reject Claims 10-14, 16-23, 25-27, 68-71 and 76-83 under 35 USC §112, first paragraph, as failing to comply with the enablement requirement. This rejection is respectfully traversed.

In particular, the Examiner alleges that "Claims 10-14, 16-23, 25-27, 68-71 and 76-83, disclosed that a first conductive layer comprising a gate electrode and a separate second conductive layer comprising gate wiring and they are in contact with each other. However, neither the specification nor the drawings disclose how they are in contact with each other." Further, the "Examiner still argues that the specification lacks a proper disclose for how a first conductive layer comprising a gate electrode and a separate second conductive layer comprising a gate wiring in contact with each other". Applicants respectfully disagree and traverse this rejection.

In particular, in one embodiment of the present application, the gate electrodes of the pixel TFT and the driver circuit TFT are in electrical contact with gate wirings comprising a second conductive layer in areas outside channel-forming regions of the pixel TFT and the driver circuit TFT. In order to advance the prosecution of this application, Claim 10 has been amended to clearly recite this feature. This feature is clearly supported and disclosed, for example, in Figs. 8B and 9B and at page 17, line 25 to page 18, line 7 of the original specification. For instance, with regard to Figs. 8B and 9B and at page 17, line 25 to page 18, line 7 of the specification as originally filed, the gate electrodes 131, 128 of the pixel TFT 204 and the driver circuit TFT 201 are in electrical contact with gate wirings 148, 147 comprising a second conductive layer in areas outside channel-forming regions 218 (219), 206 of the pixel TFT 204 and the driver circuit TFT 201. Hence, independent Claim 10 is clearly supported and enabled by the present application as filed. For similar reasons, independent claims 19, 68, 76 and 80 are also clearly described in the original specification and drawings in such a way as to enable one skilled in the art to make and/or use the invention.

Therefore amended independent Claims 10, 19, 68, 76 and 80 and those claims dependent thereon satisfy the enablement requirement. Accordingly, it is respectfully requested that this rejection be withdrawn.

Information Disclosure Statement

Applicant is submitting an information disclosure statement (IDS) herewith. It is respectfully requested that this IDS be entered and considered prior to the issuance of any further action on this application.

Conclusion

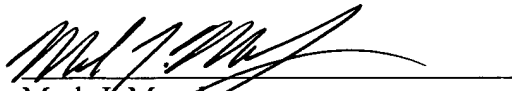
It is respectfully submitted that the present application is in a condition for allowance and should be allowed.

If any fee is due for this amendment, please charge our deposit account 50/1039.

Favorable reconsideration is earnestly solicited.

Respectfully submitted,

Date: June 26, 2006


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